Optimal Design of Hybrid Filterbank Analog-to-Digital Converters using Input Statistics

Damián Marelli, Kaushik Mahata and Minyue Fu Fellow, IEEE
School of Electrical Engineering and Computer Science, University of Newcastle, N.S.W. 2308, Australia.

Abstract—The hybrid filterbank architecture permits implementing accurate, high speed analog-to-digital converters. However, its design is technically involved since perfect reconstruction of the desired samples cannot be achieved in general. In this paper we propose a design method which generalizes existing approaches by dropping the bandlimited assumption on the input signal. The design minimizes the power of the reconstruction error in the samples, for a given input signal power spectrum. We discuss the use of blind techniques to estimate the analysis filterbank parameters as well as the input power spectrum, and we present simulation results to demonstrate the clear advantage of the proposed design, even under input spectrum uncertainties.

Index Terms—Analog digital conversion, hybrid filter banks, least mean square methods, multirate systems, multichannel sampling.

I. INTRODUCTION

A high speed analog-to-digital converter (ADC) can be realized by using the so-called time-interleaved architecture [1], which consists of using a number of parallel ADCs having the same sampling rate but different sampling phases, as if they were a single ADC operating at a higher sampling rate. A drawback of this approach is its extreme sensitivity to converter mismatches and timing errors [2]. To overcome this limitation, the hybrid filterbank ADC (HFB-ADC) architecture was proposed in [3]. This technique uses a continuous-time analysis filterbank to split the input signal into different frequency bands, each of which is assigned to a different ADC. Then, a discrete-time synthesis filterbank is used to reconstruct the required samples.

A method for designing the discrete-time synthesis filterbank was proposed in [3], and improved in [4], and relies on the assumption that the input signal is bandlimited. Under this assumption, both methods are able to achieve perfect reconstruction if the impulse response of the synthesis filterbank can be arbitrarily large. An arguable point of this approach is that the bandlimited assumption might not be realistic in many applications. To address this issue, we propose in this paper a synthesis filterbank design method which uses the knowledge of the power spectrum of the input signal, to carry out a compensation in a statistically optimal (least-mean-squares (LMS)) sense. Also, in view of our non-bandlimited scenario, the proposed method permits designing the synthesis filterbank so that the reconstructed samples match those that would be obtained if the input signal was passed through a prescribed anti-alias filter before sampling. In addition, we show that the existing design method derived under a bandlimited assumption is a particular case of the proposed method.

The proposed method, as well as the methods in [3], [4], require the knowledge of the analysis filterbank parameters. Assuming this knowledge is unrealistic since the parameters of analog circuits are subject to imperfections, e.g., deviations from nominal values, aging, temperature drifts, etc. In view of this, a reference input was used in [5] to estimate the analog parameters. However, as pointed out in [4], a blind estimation technique (i.e., without the knowledge of the input signal) is preferred. In Section V we show how to use the blind estimation method in [6], to estimate not only the analysis filterbank parameters, but also the input power spectrum.

The rest of the paper is organized as follows. We give an overview of hybrid filterbank ADCs in Section II. In Section III we introduce the proposed synthesis filterbank method. In Section IV we show that the existing design method derived under a bandlimited assumption is a particular case of the proposed method. Finally, some simulation results are presented in Section VI and concluding remarks are given in Section VII.

II. HYBRID FILTERBANK ANALOG-TO-DIGITAL CONVERTERS

The HFB-ADC scheme is depicted in Figure 1. The continuous-time signal \( x(t) \) is split into \( M \) signals using an array of continuous-time filters \( h(s) = [h_1(s), \ldots, h_M(s)]^T \), whose outputs are then sampled at rate \( 1/MT \). In this way, the discrete-time signals \( x(k) = [x_1(k), \ldots, x_M(k)]^T \) are generated. The idea is to combine the signals \( x(k) \) to generate an estimate \( \hat{y}(k) \) of the samples \( y(k) = x(kT) \). This is typically done by upsampling the signals \( x(k) \) by a factor of \( M \) (i.e., \( M - 1 \) zero valued samples are added between every two samples). Then filtering each component using the array of discrete-time filters \( f(z) = [f_1(z), \ldots, f_M(z)]^T \), and finally adding together all the resulting signals.

An approach for designing the synthesis filterbank \( f(z) \) was proposed in [3], and improved in [4]. This method assumes that the signal \( x(t) \) is bandlimited to \( 1/2T \). Under
Analysis Filterbank  Synthesis Filterbank

\[ x(t) \rightarrow h_1(s) \rightarrow \cdots \rightarrow h_M(s) \rightarrow \mathbf{F} \rightarrow \mathbf{F}^T \rightarrow \mathbf{A} \rightarrow y(k) \]

\[ y(k) = \hat{y}(k) + e(k) \]

\[ e(k) = y(k) - \hat{y}(k) \]

\[ \mathbf{F} = \arg\min_{\mathbf{F}} \mathcal{E} \left\{ \left\| y_P(0) - \sum_{l=0}^{b} \hat{F}(l)x(-l) \right\|^2 \right\} \]

\[ \mathbf{R}_{y_P}(k) = \sum_{l=0}^{b} \mathbf{F}(l)\mathbf{R}_x(k-l), \text{ for all } k \in \{a, \cdots, b\}, \]
F(k) of the polyphase matrix F(z) can be obtained by solving the linear problem (5). To this end, we need the expressions of \( R_x(k) \) and \( R_{y,y}(k) \). It is straightforward to verify that

\[
\begin{align*}
R_x(k) &= \mathcal{L}^{-1}\left\{h(s)\phi_x(s)h^T(-s)\right\}(kDT) \quad (6) \\
R_{y,y}(k) &= \mathcal{L}^{-1}\left\{h(s)\phi_x(s)h^T(-s)\right\}(kDT) \quad (7)
\end{align*}
\]

where \( \phi_x(s) = [1, e^{-s}, \cdots , e^{-(M-1)s}] \) and \( \mathcal{L}^{-1}\{\cdot\} \) denotes the inverse Laplace transform.

IV. BANDLIMITED CASE

In this section we show that the synthesis filterbank design (1) is equivalent to our proposed design (5)-(7) when \( M = D \), \( g(z) = 1 \) and the input power spectrum is given by

\[
\phi_x(j\omega) = \begin{cases} 
1, & |\omega| < \frac{\pi}{T} \\
0, & \text{otherwise}
\end{cases} \quad (8)
\]

Under these assumptions, it holds that \( x(t) = \sum_{k=-\infty}^{\infty} y(k) \text{sinc}(\frac{T}{2} - k) \), and therefore

\[
x(k) = \sum_{l=-\infty}^{\infty} h(l) y(k-l), \quad (9)
\]

where \( h(k) \) is the impulse response of the discrete-time equivalent analysis filterbank (2). Let \( y(k) \), \( \tilde{y}(k) \) and \( g(k) \) denote truncated realizations of \( y(k) \), \( \tilde{y}(k) \) and \( e(k) \), respectively, so that their \( z \)-transform \( y(z) \), \( \tilde{y}(z) \) and \( g(z) \) are well defined on the unit circle. Then, using the alias representation [8], we can write

\[
\tilde{y}(z) = \frac{1}{M} f^T(z) H_A(z) \tilde{y}_A(z)
\]

where \( H_A(z) \) and \( \tilde{y}_A(z) \) denote the alias representations of \( h(z) \) and \( \tilde{y}(z) \), respectively, which are given by

\[
H_A(z) = [h(z), h(e^{2\pi j T}), \cdots , h(e^{2\pi j (N-1)T})]
\]

\[
\tilde{y}_A(z) = [\tilde{y}(z), \tilde{y}(e^{2\pi j T}), \cdots , \tilde{y}(e^{2\pi j (N-1)T})]^T
\]

Now, letting \( J = [1, 0, \cdots , 0] \), we can write \( \tilde{y}(z) = J \tilde{y}_A(z) \) and \( e(z) = (\frac{1}{M} f^T(z) H_A(z) - J) \tilde{y}_A(z) \). Now, (8) implies that \( \tilde{y}_A(k) \) is a white vector random process, then the LMS criterion for designing \( f(z) \) becomes

\[
f = \arg \min_{\tilde{f}} \int_{-\pi}^{\pi} \left\| \frac{1}{M} f^T(e^{j\omega}) H_A(e^{j\omega}) - J \right\|_2^2 d\omega
\]

which, in view of (10), is equivalent to (1).

V. BLIND ESTIMATION OF THE INPUT SPECTRUM AND THE ANALYSIS FILTERBANK

A blind estimation method for rational models was proposed in [6]. This method can be used for estimating the analysis filterbank parameters as well as the input power spectrum, following the idea sketched below. For more details see [6].

We assume that the input power spectrum can be factorized as follows \( \phi_x(s) = |l_x(s)|^2 \), with \( l_x(s) \) being a rational model whose numerator and denominator have a known order. If the orders of the analysis filterbank filters \( h_m(s) \), \( m = 1, \cdots , M \) are also known, we can write parametric versions \( h_m(s, \alpha) \) and \( l_x(s, \beta) \), where \( \alpha \) and \( \beta \) denote the vectors of numerator and denominator coefficients of \( h_m(s) \) and \( l_x(s) \), respectively. Let \( \hat{R}_x(k) \) be an estimate of the input correlation matrix (6) obtained from the available samples. Then, using (6), \( \alpha \) and \( \beta \) can be estimated by solving the following minimization problem:

\[
(\alpha, \beta) = \arg \min_{\alpha, \beta} \sum_{k=0}^{K} ||R_x(k) - R_x(k, \alpha, \beta)||^2
\]

\[
R_x(k, \alpha, \beta) = \mathcal{L}^{-1}\left\{h(s, \alpha)l_x(s, \beta)^2h^T(s, \alpha)\right\}(kDT)
\]

VI. SIMULATION

In order to evaluate the proposed LMS design method, we compare its performance with that of the design (1), derived under a bandlimited assumption on the input signal, which we denote by (BL). The comparison is made in terms of the signal-to-distortion ratio (SDR) of the reconstructed samples, defined by

\[
SDR = 10\log_{10}\left(\frac{\sum_{k=1}^{N}|y(k)|^2}{\sum_{k=1}^{N}|y(k) - \tilde{y}(k)|^2}\right)
\]

Following [4], we consider an eight-channel HFB-ADC, where for simplicity, we use the sampling period \( T = 1 \). The analysis filterbank is composed of Butterworth second-order bandpass filters of bandwidth 1/16 Hz, except for the first one which is a first-order lowpass filter of the same bandwidth. The bandwidths are contiguously allocated so that they cover the whole frequency range from 0 Hz to 0.5 Hz. The output of each filter is sampled at 1/8 Hz (i.e., the upsampling factor \( D \) in Figure 2 equals the number \( M \) of channels). Finally, we take \( a = -3 \) and \( b = 4 \) in (4), which results in the discrete-time filters \( f_m(z) \), \( m = 1, \cdots , M \) having 64 taps with 31 non-causal taps.

In the first simulation we use \( g(s) = 1 \), and we generate the input signal as filtered white noise using a Butterworth lowpass filter \( (input \ filter) \) of 20-th order and varying cutoff frequency \( f_c \). The frequency response of a family of such filters with different values of \( f_c \) is shown in Figure 4. We compare the performances of the BL and the proposed LMS method for several values of \( f_c \). The result is shown in Fig. 5. We see how the LMS method clearly outperforms the BL method, especially for low cutoff frequency values.

![Figure 4. Frequency response of a family of input filters with different cutoff frequency values.](image)
the synthesis filterbank. In the second simulation we evaluate its performance degradation when there is a mismatch between the actual input signal power spectrum, determined by the actual input filter, and the nominal input power spectrum used to design the LMS compensator, determined by a nominal filter. We design the nominal input filter as a Butterworth lowpass filter of 20-th order and varying cutoff frequency $f_c$. For the actual input filter we use a Butterworth lowpass filter of 10-th order and varying cutoff frequency $f_c$ in cascade with a second order filter with poles in $-0.0782 f_c \pm j0.4938 f_c$. The frequency responses of the nominal and the actual input filters are shown in Fig. 6, and the simulation result is shown in Fig. 7. We see that, while not being optimal, the performance of the LMS method does not deteriorate significantly, and it is still clearly superior to that of the BL method.

In the final simulation we evaluate the performance of the proposed method when reconstructing the samples that would be obtained after filtering the input signal using a prescribed anti-alias filter $g(s)$. We use a Butterworth lowpass filter of 5-th order and varying cutoff frequency as input filter, and a Butterworth lowpass filter of 20-th order and fixed cutoff frequency $0.4 f_c$ as anti-alias filter. The obtained SDR values for different cutoff frequencies are shown in Figure 8. As expected, the performance of the LMS method improves with the use of the anti-alias filter.

VII. Conclusion

We proposed a synthesis filterbank design method for hybrid filterbank analog-to-digital converters, which generalizes existing approaches by dropping the bandlimited assumption on the input signal. The design is done in a statistically optimal sense, by minimizing the power of the sample reconstruction error, for a given input power spectrum. We discussed the use of blind techniques to estimate the analysis filterbank parameters and the input power spectrum, which are required by the proposed method, and we presented simulation results which demonstrate its clear advantage, even under uncertainties on the input power spectrum.

REFERENCES